

5

4

3

2

1

D

D

C

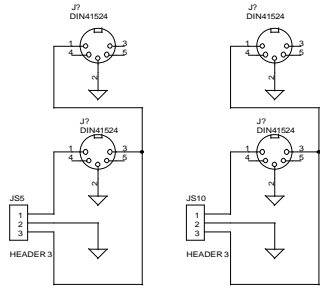
C

B

B

A

A



Title <Title>		
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Date:	Friday, October 02, 2009	Sheet 1 of 1

5

4

3

2

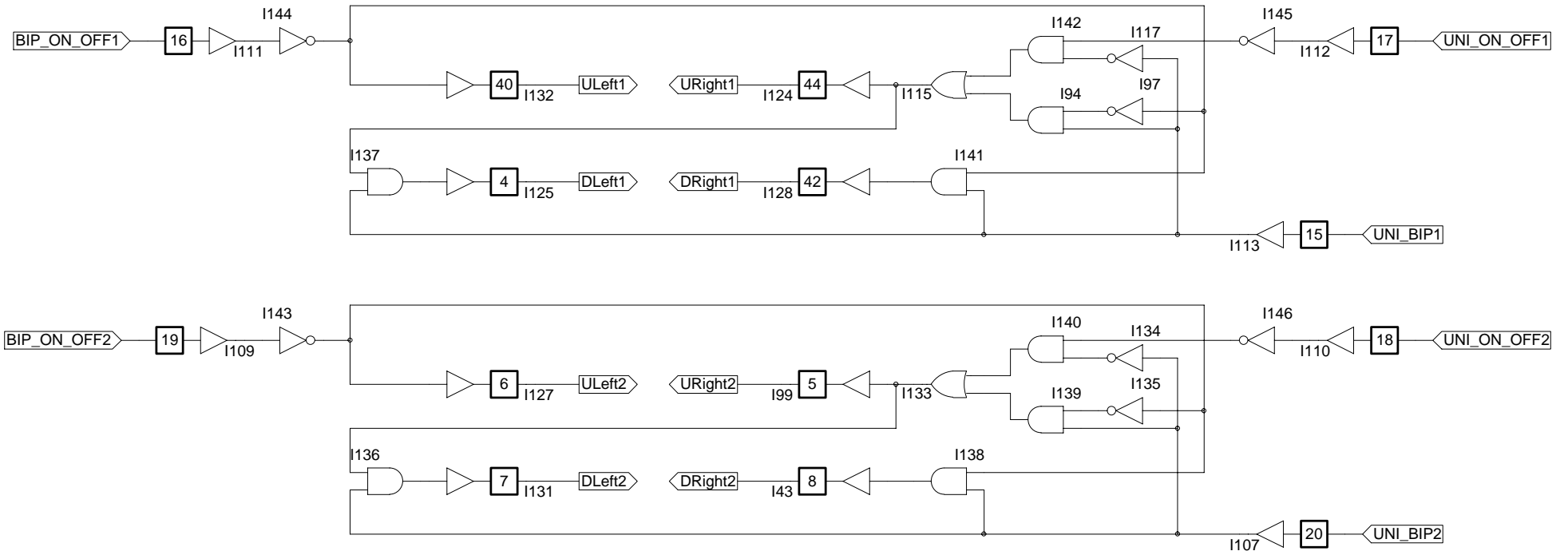
1

Revised: Friday, October 02, 2009

Revision:

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Item	Quantity	Description	Reference	Value
1	7	Ceramic Cap. 50V p.5.08	C1,C3,C4,C5,C6,C8,C9	100n
2	2	Electrolytic Cap. 35V p.7.62	C2,C7	100u
3	2	Tantalum Cap. 25V p.2.54	C10,C11	10u
4	12	Diode D1,D2,D3,D4,D5,D6,D7,D8, D9,D10,D11,D12	1N4007	
5	4	Connettore bnc da pcb 90gradi	JP1,JP2,JP3,JP4	TTL IN
6	4	Header 3P p.2.54	JS5,JP5,JS10,JP10	HEADER 3
7	2	Header 2P p.2.54	JP7,JP6	HEADER 2
8	3	Jumper	JP8,JP9,JP11	JUMP+1
9	1	Connector	J1	DIN41612_H15
10	4	Connector DIN 5P F DIN41524	J1,J2,J3,J4	DIN41524
11	1	Inductor	L1	VK200
12	4	Mosfet N	Q1,Q2,Q3,Q4	IRF1010
13	4	Trimmer orizz 1 giro 63P	R1,R2,R11,R12	500K
14	6	Res 0.25W 1%	R3,R8,R9,R10,R17,R18	10K
15	4	Res 0.25W 1%	R4,R5,R13,R14	4K3
16	4	Res 0.25W 1%	R6,R7,R15,R16	1K
17	4	Circuito Integrato	U1,U2,U4,U5	DRV102T
18	1	Circuito Integrato	U3	ispLSI2032VE-200LJ44



ispLSI2032-200LJ44 Device
 LOGIC DIAGRAM